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<input type="checkbox"/>	L2	L1 and conversion	2150
<input type="checkbox"/>	L1	32?bit and 64?bit	6595

END OF SEARCH HISTORY



1. [64-Bit Application Development for PA-RISC & IA-64 \(PDF\)](#)
... the compiler can inline the import stub. when it knows ... runtime treats. 32-bit code and 64-bit code equally ... bit addressing. • Porting to 64 bits may ...
h21007.www2.hp.com/dspp/files/unprotected/64bitAppDev.pdf - More from this site - Save - Block
2. http://www.grbb.polymtl.ca/syst_info/sg/Compilation%20sur%20R8000
... Application Porting and Transition Guide to Power ... linker could insert a stub if required to move ... this discussion, 32-bit code means mips1 or mips2, 64 bit code means ...
www.grbb.polymtl.ca/syst_info/sg/Compilation sur R8000 - More from this site - Save - Block
3. [Slashdot | 64-bit Linux On The Opteron](#)
64-bit Linux On The Opteron – article related to AMD, Hardware, and Linux. ... difference between porting the kernel and porting a distro ... equal, 64-bit code is usually about 5% slower than 32-bit code ... would be a stub procedure that maps in ...
slashdot.org/articles/03/12/16/2340207.shtml?tid=106&tid=137&... - 165k - Cached - More from this site - Save - Block
4. [The Old New Thing : Why did the Win64 team choose the LLP64 model?](#)
... problem (well, not this problem) with porting to 64 bit ... not to break existing 32-bit code*." Given this, i ... simply creates a 32bit stub for 64-bit platforms and ...
blogs.msdn.com/oldnewthing/archive/2005/01/31/363790.aspx - 155k - Cached - More from this site - Save - Block
5. [Slashdot | Articles | AMD's New SledgeHammer: 64 bit chip](#)
... try to limit myself to writing stub functions only. ... able to write current 32 bit code to work on ... is ported, even Linus agrees that porting Linux is easy ...
slashdot.org/articles/99/10/05/0834228_F.shtml - More from this site - Save - Block
6. [Ace's Hardware - General Message Board](#)
... access. in 32 bit code and thus in the ... if the. 64 bit code can make up for ... thing the 32 bit stub function needs do save ...
www.aceshardware.com/forums/read_post.jsp?id=80056899&forumid=1 - More from this site - Save - Block
7. [HP C/HP-UX Compiler Online Help - B.11.11.04: Compiling & Running HP C Programs](#)
... Generates 32-bit code for PA1.1 architecture. +DD64. Generates 64-bit code ... for compatibility in porting Sun- and SGI ... 0 generates 32-bit code for the ...
docs.hp.com/en/1694/comp_run.html - 113k - Cached - More from this site - Save - Block
8. [HP C/HP-UX Compiler Online Help : Compiling & Running HP C Programs](#)
... Generates 32-bit code for PA1.1 architecture. +DD64. Generates 64-bit code ... for compatibility in porting Sun- and SGI ... 0 generates 32-bit code for the ...
docs.hp.com/en/5921/comp_run.html - 116k - Cached - More from this site - Save - Block
9. [HP C/HP-UX Compiler Online Help - B.11.01.21: Compiling & Running HP C Programs](#)
... Generates 32-bit code for PA1.1 architecture. +DD64. Generates 64-bit code ... for compatibility in porting Sun- and SGI ... return through an export stub. The main ...
www.hsa.ir.tudelft.nl/facilities/manuals/ansic/comp_run.html - More from this site - Save - Block
10. [Linking Programs in a Single Address Space](#)
... load time with pointers to stub code. These stubs, when ... on the Irix. 32-bit code is inherently faster on ... Irix does support 64-bit code on our platform ...
www.cse.unsw.edu.au/~disy/papers/Deller_Heiser_99 - 55k - Cached - More from this site - Save - Block

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\index{C!structures} \index{packing!binary!data} This module performs
... \ctype{float} and \ctype{double} are **32-bit** and **64-bit** IEEE floating point ...
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[ps] A fast and flexible software library for large integer arithmetic ...

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A number of non-portable extensions written in **32-bit** 80x86-assembly language
... work on ANSI C compilers having **32-bit** integers and **64-bit** long integers, ...
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The Silicon Graphics Octane2 visual workstation is powered by the **64-bit** MIPS® R14000ATM ... Patented real-time color space **conversion** from RGB to YUV ...
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[PDF] ETSI/SAGE Specification Version: 1.0 Date: 30 May 2002 ...

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Table 2. A3 output. Parameter. Size (bits) Comment. K. C ... Function #2 is the
result of applying this standard **conversion** function to a **32-bit** XRES equal ...
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Mandatory attributes, storage layout and supported data types for ...

... depending on whether you are running a **32-bit** or **64-bit** architecture. ...
See the **Table** format description in section C.3.1 for more info about these ...
pytables.sourceforge.net/html-doc/x5230.html - 26k - [Cached](#) - [Similar pages](#)

Java programming: Fun with Fibonacci

Switching to floating-point arithmetic only helps a little: **32-bit** IEEE 754 ...
Ada and Java support **64-bit** integers natively, while only some C, C++, ...
www.math.utah.edu/~beebe/software/java/fibonacci/ - 57k - Jul 20, 2005 - [Cached](#) - [Similar pages](#)

Fortran Compiler for Macintosh OS X

If you require code for **32-bit** or **64-bit** environments, Pro Fortran v9.2 provides a
... Pro Fortran includes: Fortran 95 (64/32-bit) and C/C++ compilers ...
www.absoft.com/Products/Compilers/Fortran/Macintosh/OSX/OSX.html - 101k - Jul 19, 2005 - [Cached](#) - [Similar pages](#)

Porting to a **64-bit Platform, Part 3: Porting your Application ...**

You will, however, see major performance differences comparing a **32-bit** ...
both FORTRAN and C, so it may not be obvious that float-to-integer **conversion** is ...
www.intel.com/cd/ids/developer/asmo-na/eng/dc/64bit/200519.htm?page=13 - 69k - [Cached](#) - [Similar pages](#)

DSP: Center: Digital Signal Processing (DSP) News

Consider a 24- or **32-bit** DSP to Improve Digital Audio (Part 2 of 3) ... **Table 1** -
Comparison of extended dynamic range in fixed-point DSP multiplier unit ...

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